

**IN THE UNITED STATES PATENT AND TRADEMARK
OFFICE**

In re: Hsin-Hui Lee
Ser. No.: 10/725,697
Filed: December 1, 2003
Docket No.: N1280-00320 (TSMC2003-1469)
Title: METHOD FOR DICING SEMICONDUCTOR WAFERS

The Patent Office acknowledges and has stamped hereon the date of receipt of
the following items:

Response to Restriction Requirement

MJM:mep

January 20, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Hsin-Hui Lee

Serial No.: 10/725,697

Filed: December 1, 2003

Group Art Unit: 2822

Examiner: Toniae M. Thomas

Confirmation No.: 9019

For: METHOD FOR DICING
SEMICONDUCTOR WAFERS

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date

1/20/05
Maria E. Provencio
Maria E. Provencio

Examiner, Toniae M. Thomas
Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Dear Sir:

Responsive to the Restriction Requirement in the Office Action dated December 22, 2004, applicants elect to further prosecute the above-identified application as follows:

The Examiner requires an election between:

Group I: Claims 1-39, drawn to a process of making, classified in class 438, subclass 106.

Group II: Claims 40-53, drawn to a product, classified in class 257, subclass 678.

Applicants elect Group I, claims 1-39 for further prosecution.


PATENT

Office Action Date: December 22, 2004

In view of the foregoing, Applicants request that claims 1-39 should be examined.

Respectfully submitted,

DATE: 20 January 05


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